

? show files

[File 347] JAPIO Dec 1976-2008/Aug(Updated 081208)

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[File 350] Derwent WPIX 1963-2008/UD=200880

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; d s

Set Items Description

S1 141 S (KERNEL(3N)(EMULAT? OR SIMULATOR? ? OR VIRTUAL))

S2 23517 S ("NON" ())NATIVE OR NONNATIVE OR NATIVE)

S3 388 S (CONVERT? OR CONVERSION? ? OR CHANGE?? OR CHANGING OR TRANSLAT? OR CHANG? OR ALTER? OR ALTERATION? ? OR MODIF? OR TRANSFORM? OR REPLAC??? OR SUBSTITUT???) (2N)KERNEL? ?

S4 4 S S3(20N)S2

S5 1 S S4(20N)S1

S6 14 S S3 AND S1

S7 3 S S1 AND (AU=(BOND, B? OR BOND B OR KHALID, A? OR KHALID A? OR KHALID, S? OR KHALID S?))

?

Subject summary

? t/3,k/all

6/3,K/1 (Item 1 from file: 347) [Links](#)Fulltext available through: [Order File History](#)

JAPIO

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05673783 **Image available**

INFORMATION PROCESSOR

Pub. No.: 09-288583 [JP 9288583 A]

Published: November 04, 1997 (19971104)

Inventor: AKAMATSU JUNKO

Applicant: NEC CORP [000423] (A Japanese Company or Corporation), JP (Japan)

Application No.: 08-122604 [JP 96122604]

Filed: April 19, 1996 (19960419)

ABSTRACT

...storing a system firmware 51 and extended device 60 through a system bus 100, a kernel interface emulation executing part 513 is provided for converting only a kernel interface function part 412 at one part of the operating system 41 stored in the... DiO1

6/3,K/2 (Item 1 from file: 350) [Links](#)Fulltext available through: [Order File History](#)

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0017894607 & & Drawing available

WPI Acc no: 2008-H14925/200846

XRAM Acc no: C2008-225157

XRPX Acc No: N2008-567858

Offline simulation system for continuous slab casting has computer with model counting kernel unit and technology model parameter enactment unit that alternate with technology model parameters through model parameter database

Patent Assignee: SHANGHAI MEISHAN STEELS CO LTD (SHAN-N)

Inventor: GUO Z; JI C; JIANG Z; WANG H; ZHAO Q; ZHU M

Patent Family (1 patents, 1 & countries)

| Patent Number | Kind | Date | Application Number | Kind | Date | Update | Type |
|---------------|------|----------|--------------------|------|----------|--------|------|
| CN 101169624 | A | 20080430 | CN 200610117522 | A | 20061025 | 200846 | B |

Priority Applications (no., kind, date): CN 200610117522 A 20061025

Patent Details

| Patent Number | Kind | Lan | Pgs | Draw | Filing Notes |
|---------------|------|-----|-----|------|--------------|
| CN 101169624 | A | ZH | 11 | 4 | |

Alerting Abstract ...NOVELTY - A computer has a simulator kernel unit and a model counting kernel unit that alternate with process parameter data through a shared memory. The model counting kernel unit and a... DESCRIPTION - The simulator kernel unit simulates process parameters of a main casting condition. The technology model parameter enactment unit... Original Publication Data by AuthorityArgentinaPublication No. ...Original Abstracts:processing dynamic soft pressure during real production. The computer of the simulation system comprises: a simulator kernel unit, used for realizing the simulation of the process parameters of the main casting condition... ..monitoring displaying unit, used for mainly finishing the interface displaying of the simulation result; the simulator kernel unit and the model counting kernel unit through the share memory to realize the alternation... ..Claims:continuous slab casting and dynamic soft pressure, comprising: a computer, wherein said comprises comprising: a simulator kernel unit, used for realizing the simulation of the process parameters of the main casting condition... ..monitoring displaying unit, used for mainly finishing the interface displaying of the simulation result; the simulator kernel unit and the model counting kernel unit through the share memory to realize the alternation...

6/3,K/3 (Item 2 from file: 350) [Links](#)Fulltext available through: [Order File History](#)

Derwent WPIX

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0017318539 & & Drawing available

WPI Acc no: 2008-B38980/200809

XRPX Acc No: N2008-108530

Simulation system for mobile phone, has plug-in module, simulator module and man-machine-interface kernel module that perform reception and transmission of data through communication protocols

Patent Assignee: SUNPLUS TECHNOLOGY CO LTD (SUNP-N)

Inventor: KO Y

Patent Family (1 patents, 1 & countries)

| Patent Number | Kind | Date | Application Number | Kind | Date | Update | Type |
|---------------|------|------|--------------------|------|------|--------|------|
|---------------|------|------|--------------------|------|------|--------|------|

| | | | | | | | |
|----------------|----|----------|---------------|---|----------|--------|---|
| US 20080020750 | A1 | 20080124 | US 2007780487 | A | 20070720 | 200809 | B |
|----------------|----|----------|---------------|---|----------|--------|---|

Priority Applications (no., kind, date): TW 2006126654 A 20060721

Patent Details

| Patent Number | Kind | Lan | Pgs | Draw | Filing Notes |
|----------------|------|-----|-----|------|--------------|
| US 20080020750 | A1 | EN | 8 | 4 | |

Original Publication Data by AuthorityArgentinaPublication No. ...Original Abstracts:phone and method thereof is provided. The system has separately designed man-machine-interface (MMI) kernel module, simulator module and plug-in module. If a designer wants to replace the MMI kernel module in a developing project, the present invention allows not only the transplantation and extension...

6/3,K/4 (Item 3 from file: 350) [Links](#)

Fulltext available through: [Order File History](#)

Derwent WPIX

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0015610563 & *Drawing available*

WPI Acc no: 2006-174736/200619

XRPX Acc No: N2006-150496

Converter imitating system for steelmaking production simulation, has converter simulation kernel, plan control simulator, monitor, algorithm model base, smart card memory and user interactive interface

Patent Assignee: RES & DESIGN INST METALLURGICAL AUTOMATI (REDE-N)

Inventor: JI Y; LI W

Patent Family (2 patents, 1 & countries)

| Patent Number | Kind | Date | Application Number | Kind | Date | Update | Type |
|---------------|------|----------|--------------------|------|----------|--------|------|
| CN 1687953 | A | 20051026 | CN 200510070864 | A | 20050520 | 200619 | B |
| CN 1326040 | C | 20070711 | | | | 200807 | E |

Priority Applications (no., kind, date): CN 200510070864 A 20050520

Patent Details

| Patent Number | Kind | Lan | Pgs | Draw | Filing Notes |
|---------------|------|-----|-----|------|--------------|
| CN 1687953 | A | ZH | | 1 | |

Converter imitating system for steelmaking production simulation, has converter simulation kernel, plan control simulator, monitor, algorithm model base, smart card memory and user interactive interface Alerting Abstract ...converter simulating system, belonging to the field of production simulation technique for steelmaking, composed of converter simulation kernel, plan control simulator, monitor, algorithm model base, smart card memory and user interactive interface, all interconnected by the Ethernet LAN, where the converter simulation kernel implements the simulation of the producing course of a converter; the plan control simulator plays...

6/3,K/5 (Item 4 from file: 350) [Links](#)

Fulltext available through: [Order File History](#)

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0015067626 & *Drawing available*

WPI Acc no: 2005-416861/200542

XRPX Acc No: N2005-338208

Method for measuring performance in data processing system having kernel including virtual machine involves defining suite of micro benchmarks such that each set when executed, provides runtime estimate for corresponding instruction

Patent Assignee: BLUMENTHAL A (BLUM-I); LUEDDE M (LUED-I); MANZKE T (MANZ-I); MIELENHAUSEN B (MIEL-I); SWANEPOEL C E (SWAN-I)

Inventor: BLUMENTHAL A; LUEDDE M; MANZKE T; MIELENHAUSEN B; SWANEPOEL C E

Patent Family (1 patents, 1 & countries)

| Patent Number | Kind | Date | Application Number | Kind | Date | Update | Type |
|----------------|------|----------|--------------------|------|----------|--------|------|
| US 20050120341 | A1 | 20050602 | US 2003500282 | P | 20030903 | 200542 | B |
| | | | US 2004934940 | A | 20040903 | | |

Priority Applications (no., kind, date): US 2003500282 P 20030903; US 2004934940 A 20040903

Patent Details

| Patent Number | Kind | Lan | Pgs | Draw | Filing Notes |
|----------------|------|-----|-----|------|--------------------------------------|
| US 20050120341 | A1 | EN | 15 | 3 | Related to Provisional US 2003500282 |

Method for measuring performance in data processing system having kernel including virtual machine involves defining suite of micro benchmarks such that each set when executed, provides runtime... Alerting Abstract

...also included for a system for measuring performance in a data processing system having a kernel including a virtual machine ... USE - For measuring performance in a data processing system having a kernel including a virtual machine... into account the workloads of individual instructions. Developers can immediately investigate the effects of minor changes to the kernel on the performance of complex software systems. Consistency of other existing benchmarks between measurements such... Original Publication Data by AuthorityArgentinaPublication No. Claims:1. A method of measuring performance in a data processing system having a kernel including a virtual machine, the method comprising:obtaining an instruction profile of a computer program application running on a virtual machine, the...

6/3,K/6 (Item 5 from file: 350) [Links](#)

Fulltext available through: [Order File History](#)

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0015017717 & & *Drawing available*

WPI Acc no: 2005-365670/200537

Related WPI Acc No: 2002-489057

XRPX Acc No: N2005-296410

Kernel emulator for operating system, has translator selector to select translator to translate non-native instructions into native instructions, and target-platform simulator to simulate target platform of program module

Patent Assignee: MICROSOFT CORP (MICT)

Inventor: BOND B; KHALID A S

Patent Family (1 patents, 1 & countries)

| Patent Number | Kind | Date | Application Number | Kind | Date | Update | Type |
|----------------|------|----------|--------------------|------|----------|--------|------|
| US 20050102129 | A1 | 20050512 | US 2000244410 | P | 20001030 | 200537 | B |
| | | | US 2001847535 | A | 20010501 | | |
| | | | US 200416630 | A | 20041217 | | |

Priority Applications (no., kind, date): US 2000244410 P 20001030; US 2001847535 A 20010501; US 200416630 A 20041217

Patent Details

| Patent Number | Kind | Lan | Pgs | Draw | Filing Notes |
|----------------|------|-----|-----|------|---|
| US 20050102129 | A1 | EN | 17 | 6 | Related to Provisional US 2000244410 |
| | | | | | Continuation of application US 2001847535 |

Kernel emulator for operating system, has translator selector to select translator to translate non-native instructions into... Original Titles:Kernel emulator for non-native program modules Alerting Abstract ...A target-platform simulator simulates the platform so that kernel calls from the module are converted into native kernel calls. ... an operating system on a computer-readable medium comprising a kernel emulator a computer-readable media having computer-executable instructions that, when executed by a computer, perform kernel emulation acts for non-native program modules. ... DESCRIPTION OF DRAWINGS - The drawing shows a flow diagram depicting a methodological implementation of a kernel emulator.Original Publication Data by AuthorityArgentinaPublication No. ...Original Abstracts:non-native program modules within a native computing platform. More specifically, this technology involves an emulation of the kernel of the non-native operating system. Instead of interacting with the native kernel of the native computing platform, the non-native program modules interact with a non-native kernel emulator. This abstract itself is not intended to limit the scope of this patent. The scope of the present invention... Claims:1. A kernel emulator for non-native program modules, the emulator comprising:target-platform determiner configured to determine a target platform of a non-native program... simulate the selected target platform so that kernel calls from non-native program modules are converted into native kernel calls.

6/3,K/7 (Item 6 from file: 350) [Links](#)

Fulltext available through: [Order File History](#)

Derwent WPIX

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0014109979 & & *Drawing available*

WPI Acc no: 2004-294291/200427

XRPX Acc No: N2004-233783

Flexible system simulation architecture for simulating electronic device, has rendering object showing simulation results obtained by performing multiple sets of application program codes

Patent Assignee: INST INFORMATION IND (INFO-N)

Inventor: CHEN B; CHEN P W; LIAO C S; LIAU J

Patent Family (2 patents, 2 & countries)

| Patent Number | Kind | Date | Application Number | Kind | Date | Update | Type |
|----------------|------|----------|--------------------|------|----------|--------|------|
| US 20040059557 | A1 | 20040325 | US 2002325871 | A | 20021223 | 200427 | B |
| TW 576999 | A | 20040221 | TW 2002121923 | A | 20020924 | 200455 | E |

Priority Applications (no., kind, date): TW 2002121923 A 20020924

Patent Details

| Patent Number | Kind | Lan | Pgs | Draw | Filing Notes |
|---------------|------|-----|-----|------|--------------|
|---------------|------|-----|-----|------|--------------|

| | | | | | | |
|----------------|----|----|---|---|--|--|
| US 20040059557 | A1 | EN | 7 | 5 | | |
| TW 576999 | A | ZH | | | | |

Alerting Abstract ... to install different kernel of embedded system in the same target machine and provide different simulators by changing kernel and eliminates the need to re-construct all components of the simulator and thereby saves a lot of time... Original Publication Data by AuthorityArgentinaPublication No. ...Original Abstracts:Interface) is disposed between an application program object and a system kernel object. A GSI (Generic Simulator Interface) is provided between the rendering object and the system kernel object. The rendering object...

6/3,K/8 (Item 7 from file: 350) [Links](#)

Fulltext available through: [Order File History](#)

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0013601275 & & *Drawing available*

WPI Acc no: 2003-696339/200366

XRPX Acc No: N2003-556055

Machine instruction simulation system for personal computer, has monitor that translates machine instructions into translated code, and virtual machine that executes translated code

Patent Assignee: FISSTEIN R (FISH-I); LEVIT-GUREVICH K (LEVI-I); LIOKUMOVICH I (LIOK-I); RAPPOPORT R (RAPP-I)

Inventor: FISSTEIN R; LEVIT-GUREVICH K; LIOKUMOVICH I; RAPPOPORT R

Patent Family (1 patents, 1 & countries)

| Patent Number | Kind | Date | Application Number | Kind | Date | Update | Type |
|----------------|------|----------|--------------------|------|----------|--------|------|
| US 20030115578 | A1 | 20030619 | US 200125217 | A | 20011218 | 200366 | B |

Priority Applications (no., kind, date): US 200125217 A 20011218

Patent Details

| Patent Number | Kind | Lan | Pgs | Draw | Filing Notes |
|----------------|------|-----|-----|------|--------------|
| US 20030115578 | A1 | EN | 10 | 5 | |

Original Publication Data by AuthorityArgentinaPublication No. ...Original Abstracts:The simulation modules also include a virtual machine that executes the translated code, and a kernel that detects exceptions occurring in the virtual machine and transfers control between the virtual machine and the monitor according to a type... Claims:translated code from being modified; a virtual machine that executes the translated code stored in memory; and a kernel that detects exceptions occurring in the virtual machine and transfers control between the virtual machine and the monitor according to a type of the exceptions.

6/3,K/9 (Item 8 from file: 350) [Links](#)

Fulltext available through: [Order File History](#)

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0012639940 & & *Drawing available*

WPI Acc no: 2002-489057/200252

Related WPI Acc No: 2005-365670

XRPX Acc No: N2002-386585

Kernel emulator for non-native program modules, intercepts non-native kernel calls for conversion to native kernel calls

Patent Assignee: BOND B (BOND-I); KHALID A S (KHAL-I); MICROSOFT CORP (MICT)

Inventor: BOND B; KHALID A S; KHALID S A

Patent Family (2 patents, 27 & countries)

| Patent Number | Kind | Date | Application Number | Kind | Date | Update | Type |
|----------------|------|----------|--------------------|------|----------|--------|------|
| US 20020052727 | A1 | 20020502 | US 2000244410 | P | 20001030 | 200252 | B |
| | | | US 2001847535 | A | 20010501 | | |
| EP 1209564 | A2 | 20020529 | EP 2001125152 | A | 20011023 | 200252 | E |

Priority Applications (no., kind, date): US 2000244410 P 20001030; US 2001847535 A 20010501

Patent Details

| Patent Number | Kind | Lan | Pgs | Draw | Filing Notes |
|-------------------------------------|---|-----|-----|------|--------------------------------------|
| US 20020052727 | A1 | EN | 18 | 6 | Related to Provisional US 2000244410 |
| EP 1209564 | A2 | EN | | | |
| Regional Designated States,Original | AL AT BE CH CY DE DK ES FI FR GB GR IE IT LI LT LU LV MC MK NL PT RO SE SI TR | | | | |

Kernel emulator for non-native program modules, intercepts non-native kernel calls for conversion to native kernel calls ...Original Titles:Kernel emulator for non-native program modules... Kernel emulator for non-native program modules Alerting Abstract ...NOVELTY - An interceptor intercepts kernel calls from non-native program

modules. A call converter converts non-native kernel calls intercepted into native kernel calls. ... Operating system on a computer-readable medium; Kernel emulating method; and Computer comprising computer-readable media storing kernel emulating program. ... USE - Kernel emulator for non-native program modules in computer systems. Original Publication Data by AuthorityArgentinaPublication No. ... Original Abstracts:non-native program modules within a native computing platform. More specifically, this technology involves an emulation of the kernel of the non-native operating system. Instead of interacting with the native kernel of the native computing platform, the non-native program modules interact with a non-native kernel emulator. This abstract itself is not intended to limit the scope of this patent. The scope of the present invention... non-native program modules within a native computing platform. More specifically, this technology involves an emulation of the kernel of the non-native operating system. Instead of interacting with the native kernel of the native computing platform, the non-native program modules interact with a non-native kernel emulator. This abstract itself is not intended to limit the scope of this patent. The scope of the present invention is pointed out in the... Claims:A kernel emulator for non-native program modules, the emulator comprising:an interceptor configured to intercept kernel calls from non-native program modules;a call-converter configured to convert non-native kernel calls intercepted by the interceptor into native kernel calls... 1. A kernel emulator for non-native program modules, the emulator comprising:an interceptor configured to intercept kernel calls from non-native program modules;a call-converter configured to convert non-native kernel calls intercepted by the interceptor into native kernel calls.

6/3,K/10 (Item 9 from file: 350) [Links](#)

Fulltext available through: [Order File History](#)

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0010521111 & & Drawing available

WPI Acc no: 2001-122640/200113

XRPX Acc No: N2001-090085

Execution logic dynamic injection in computer system, involves executing injection function in injection hook function prior to execution of any other application related logic in created new process address space

Patent Assignee: CHICAGO SOFT LTD (CHIC-N); NOVADIGM INC (NOVA-N)

Inventor: HAMMOND R P

Patent Family (3 patents, 88 & countries)

| Patent Number | Kind | Date | Application Number | Kind | Date | Update | Type |
|---------------|------|----------|--------------------|------|----------|--------|------|
| WO 2000062141 | A2 | 20001019 | WO 2000US2929 | A | 20000203 | 200113 | B |
| AU 200026381 | A | 20001114 | AU 200026381 | A | 20000203 | 200113 | E |
| US 6463583 | B1 | 20021008 | US 1999289150 | A | 19990408 | 200269 | E |

Priority Applications (no., kind, date): US 1999289150 A 19990408

Patent Details

| Patent Number | Kind | Lan | Pgs | Draw | Filing Notes |
|-------------------------------------|--|-----|-----|------|-----------------------------------|
| WO 2000062141 | A2 | EN | 36 | 6 | |
| National Designated States,Original | AE AL AM AT AU AZ BA BB BG BR BY CA CH CN CR CU CZ DE DK DM EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT TZ UA UG US UZ VN YU ZA ZW | | | | |
| Regional Designated States,Original | AT BE CH CY DE DK EA ES FI FR GB GH GM GR IE IT KE LS LU MC MW NL OA PT SD SE SL SZ TZ UG ZW | | | | |
| AU 200026381 | A | EN | | | Based on OPI patent WO 2000062141 |

Original Publication Data by AuthorityArgentinaPublication No. ... Original Abstracts:link library within the main dynamic link library function in the kernel dynamic link library to create a modified kernel dynamic link library. The jump command jumps to an injection hook function within the injection dynamic link library whenever... the main dynamic link library function in the kernel dynamic link library to create a modified kernel dynamic link library. The jump command jumps to an injection hook function within the injection dynamic link library whenever a new windowed operating system... Claims:starting an injection application from a kernel application used by a windowed operating system;loading an injection virtual device driver from the injection application into a first pre-determined memory location in an... main dynamic link library function in the original kernel dynamic link library to create a modified kernel dynamic link library, wherein the jump command jumps to an injection hook function within the injection dynamic link library whenever a new windowed operating system process is created,and wherein the injection...

6/3,K/11 (Item 10 from file: 350) [Links](#)

Fulltext available through: [Order File History](#)

Derwent WPIX

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0010455807 & & Drawing available

WPI Acc no: 2001-055301/200107

XRPX Acc No: N2001-042806

Dynamic optimization of computer program, involves providing profile data of program to kernel module which translates program for optimization during execution, which is then patched into shared user memory space

Patent Assignee: HEWLETT-PACKARD CO (HEWP); HEWLETT-PACKARD DEV CO LP (HEWP)

Inventor: KRISHNASWAMY U; SHAH L V; UMESSHU K

Patent Family (2 patents, 2 & countries)

| Patent Number | Kind | Date | Application Number | Kind | Date | Update | Type |
|---------------|------|----------|--------------------|------|----------|--------|------|
| JP 2000315160 | A | 20001114 | JP 2000119483 | A | 20000420 | 200107 | B |
| US 6622300 | B1 | 20030916 | US 1999295548 | A | 19990421 | 200362 | E |

Priority Applications (no., kind, date): US 1999295548 A 19990421

Patent Details

| Patent Number | Kind | Lan | Pgs | Draw | Filing Notes |
|---------------|------|-----|-----|------|--------------|
| JP 2000315160 | A | JA | 15 | 7 | |

Dynamic optimization of computer program, involves providing profile data of program to kernel module which translates program for optimization during execution, which is then patched into shared user memory space. Alerting Abstract ...memory space (120), is provided to kernel module distributed in kernel memory space (100). The kernel module translates the program for optimization during computer program execution, after which the translated program is patched... Original Publication Data by AuthorityArgentinaPublication No. ...Original Abstracts:optimizations both of user programs and of the computer operating system kernel, itself. The kernel module permits optimized translations to be shared across a computer system without emulation because the kernel module has the privileges necessary to write into the computer program text in shared user memory space. In addition... Claims:data to a kernel module located in kernel memory space;generating at least one optimized translation of at least one portion of the computer program using the kernel module, the step of generating including... patching the computer program into shared user memory space using the at least one optimized translation as the computer program continues to execute.

6/3,K/12 (Item 11 from file: 350) [Links](#)

Fulltext available through: [Order File History](#)

Derwent WPIX

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0009090678 & & Drawing available

WPI Acc no: 1999-009290/199901

Related WPI Acc No: 2004-550856

XRPX Acc No: N1999-006761

Virtual memory system for operating system - includes address mapping buffer with several translation entries including dynamically changing subset of virtual physical address mappings described by task and kernel address maps

Patent Assignee: MICROSOFT CORP (MICT)

Inventor: DRAVES R P; ODINAK G

Patent Family (1 patents, 1 & countries)

| Patent Number | Kind | Date | Application Number | Kind | Date | Update | Type |
|---------------|------|----------|--------------------|------|----------|--------|------|
| US 5835964 | A | 19981110 | US 1996639773 | A | 19960429 | 199901 | B |

Priority Applications (no., kind, date): US 1996639773 A 19960429

Patent Details

| Patent Number | Kind | Lan | Pgs | Draw | Filing Notes |
|---------------|------|-----|-----|------|--------------|
| US 5835964 | A | EN | 17 | 9 | |

Original Publication Data by AuthorityArgentinaPublication No. ...Claims:stored in the mapped memory of the kernel's address space, each task address map describing a set of virtual-to-physical address mappings for a particular task's virtual address space;a kernel address map stored in the unmapped memory of the kernel's address space, the kernel address map describing a set of virtual-to-physical address mappings for the mapped memory of the kernel's address space;an address mapping buffer in the unmapped memory, the address mapping buffer... subset of the virtual-to-physical address mappings described by the task address maps and the kernel address map;a hardware-implemented translation lookaside buffer which stores a dynamically changing subset of the translation entries from the address mapping buffer;a lookaside buffer handler which...

6/3,K/13 (Item 12 from file: 350) [Links](#)

Fulltext available through: [Order File History](#)

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0008623178 & & Drawing available

WPI Acc no: 1998-159731/199814

XRPX Acc No: N1998-126888

Method of filtering digital image by segmenting image into blocks of preset size - involves overlapping adjacent blocks by preset number of pixels, small filters are generated by processing group of components of filter kernel

Patent Assignee: POLAROID CORP (INTP)

Inventor: WOBER M A

Patent Family (4 patents, 20 & countries)

| Patent Number | Kind | Date | Application Number | Kind | Date | Update | Type |
|---------------|------|----------|--------------------|------|----------|--------|------|
| WO 1998007117 | A1 | 19980219 | WO 1996US17634 | A | 19961031 | 199814 | B |

| | | | | | | | |
|-------------|----|----------|----------------|---|----------|--------|---|
| US 5748792 | A | 19980505 | US 1996696172 | A | 19960813 | 199825 | E |
| EP 855066 | A1 | 19980729 | EP 1996938741 | A | 19961031 | 199834 | E |
| | | | WO 1996US17634 | A | 19961031 | | |
| JP 11514122 | W | 19991130 | WO 1996US17634 | A | 19961031 | 200007 | E |
| | | | JP 1998509667 | A | 19961031 | | |

Priority Applications (no., kind, date): US 1996696172 A 19960813

Patent Details

| Patent Number | Kind | Lan | Pgs | Draw | Filing Notes | | |
|-------------------------------------|--|-----|-----|------|---------------------|----------------|--|
| WO 1998007117 | A1 | EN | 37 | 19 | | | |
| National Designated States,Original | CA JP | | | | | | |
| Regional Designated States,Original | AT BE CH DE DK ES FI FR GB GR IE IT LU MC NL PT SE | | | | | | |
| US 5748792 | A | EN | 23 | | | | |
| EP 855066 | A1 | EN | | | PCT Application | WO 1996US17634 | |
| | | | | | Based on OPI patent | WO 1998007117 | |
| Regional Designated States,Original | DE FR GB | | | | | | |
| JP 11514122 | W | JA | 35 | | PCT Application | WO 1996US17634 | |
| | | | | | Based on OPI patent | WO 1998007117 | |

Original Publication Data by AuthorityArgentinaPublication No. ...Claims:a preselected filter kernel; generating small filters by processing a preselected group of components of said filter kernel; generating DOCT filters by performing a discrete odd cosine transformation (DOCT) on each said small filter; generating mask multiplied blocks by mask multiplying each DCT block times... .. point-by-point filtering of said overlapped blocks with said small filters; and means for emulating filtering of the image with said large filter by combining said component data blocks.

6/3,K/14 (Item 13 from file: 350) [Links](#)

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0007687485 & & *Drawing available*

WPI Acc no: 1996-309110/199631

XRPX Acc No: N1996-259811

Virtual memory addressing control method for information appts. - having address translating section separated from kernel of OS as address translation server for calculating physical address w.r.t. process and system page tables

Patent Assignee: CANON KK (CANO)

Inventor: MIYAMOTO T

Patent Family (1 patents, 1 & countries)

| Patent Number | Kind | Date | Application Number | Kind | Date | Update | Type |
|---------------|------|----------|--------------------|------|----------|--------|------|
| US 5530821 | A | 19960625 | US 1992921225 | A | 19920729 | 199631 | B |

Priority Applications (no., kind, date): JP 1991193936 A 19910802

Patent Details

| Patent Number | Kind | Lan | Pgs | Draw | Filing Notes | | |
|---------------|------|-----|-----|------|--------------|--|--|
| US 5530821 | A | EN | 9 | 5 | | | |

Original Publication Data by AuthorityArgentinaPublication No. ...Original Abstracts:an information processing apparatus which employs the present invention, the address translating section is separated from the kernel of the operating system (OS) as an address translation server and they communicate with each... .. and system page table upon reception of a process identifier and virtual address from the OS kernel. >

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